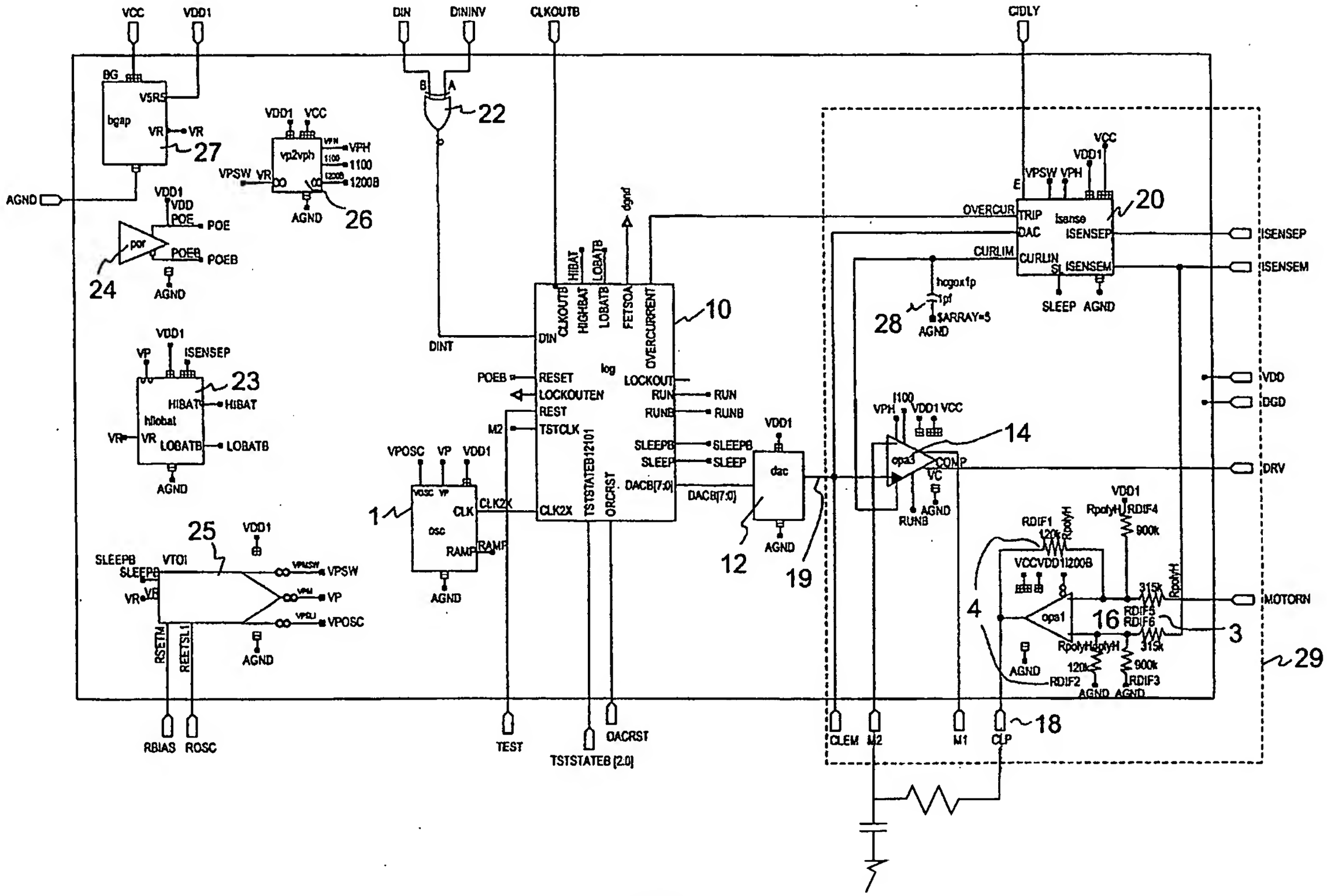
VCC
VDD1
DIN
DININV
CLKOUTB
CIDLY
BG
V5RS
bgap
VR
27
VDD1 VCC
vp2vph
VPH
1100
1200B
VPSW VR
AGND
26
AGND
VDD1
VDD
POE
POE
por
POEB
POEB
AGND
24
22
B
A
dgnd
HIBAT
LOBATB
FETSOA
CLKOUTB
HIGHBAT
OVERCURRENT
10
DINT
DIN
log
POEB
RESET
LOCKOUT
LOCKOUTEN
RUN
RUNB
REST
TSTCLK
M2
SLEEPB
SLEEP
VDD1
dac
DACB[7:0]
DACB[7:0]
12
AGND
19
VPOSC VP VDD1
CLK2X
CLK
osc
RAMP
AGND
1
TSTSTATEB[2:0]
ORCRST
VDD1
VP
ISENSEP
23
HIBAT
hibat
VR
LOBATB
AGND
25
VDD1
SLEEPB
VTOI
VPSW
VP
VPOSC
RSETM
REETSL1
AGND
RBIAS
ROSC
TEST
OACRST
TSTSTATEB[2:0]
VCC
VDD1
VPSW VPH
E
OVERCUR
TRIP
DAC
Isense
ISENSEP
20
CURLIM
CURLIN
ISENSEM
SLEEP
AGND
hcgox1p
1pf
$ARRAY=5
28
AGND
ISENSEP
ISENSEM
VDD
DGD
DRV
MOTORN
3
29
VPH
100
VDD1
VCC
14
opa3
COMP
VC
AGND
RUNB
VDD1
RpolyH
RDIF4
900k
RDIF1
120k
RpolyH
VCCVDD1I200B
8
315k
RpolyH
4
opa1
16
RDIF5
RDIF6
315k
3
RpolyH
RpolyH
AGND
120k
900k
RDIF2
RDIF3
AGND
AGND
CLEM
M2
M1
CLP
18

FIG. 1